

Customer No.: 31561  
Application No.: 10/707,163  
Docket No.: 10659-US-PA

### REMARKS

#### Present Status of the Application

Currently pending claims 1-12 are rejected. Specifically, claims 1-12 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamazaki et al. (U. S. Pub. 2002/0005696; hereinafter Yamazaki). Applicants have amended specification and independent claim 1 to correct typographic errors. Applicants have added claim 13-14. After entry of the foregoing amendments, claims 1-14 remain pending in the present application, and reconsideration of those claims is respectfully requested.

#### Discussion of Claim Rejections under 35 USC 102

Claims 1-12 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamazaki. Applicants respectfully traverse the rejections for at least the reasons set forth below.

1. In the present invention, with respect to independent claim 1, as for example shown in FIG. 5, the voltage terminal 510 is connected to a center of the power lines set 502. The other power line sets are in similar connection manner.

2. With respect to independent claim 7, as for example shown in FIG. 7, the *conductive material medium* 704 is placed between the voltage terminals 702 and the power supply 706, so as to supply voltage to all of voltages terminals with reduced voltage drop. In other words, each voltage terminal is connected to the power supply through the same conductive material medium 704.

3. With respect to newly added claim 13, claim 13 further recites that the conductive

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material medium is a conductive material interface 704 in external connection, so that the conductive material interface does not occupy the panel area.

4. With respect to newly added claim 14, claim 14 further recites that the voltage terminal 510 is connected to a center of the power lines set.

5. It should be further noted that the secondary power lines are connected to the corresponding power line set, and the power line set is applied with a driving high voltage Vdd, via the power supply and the voltage terminal. In other words, the voltage applied on the secondary power lines is the driving high voltage Vdd *but not a ground voltage or a relative low voltage to the driving high voltage Vdd*. The present invention can effectively reduce the voltage drop, so that the brightness difference between neighboring pixels can be greatly minimized.

6. In re Yamazaki, Yamazaki at least failed to disclose the foregoing feature as recited in claimed invention.

In Fig. 5, the external switch 116 is for switching the low voltage end between a ground voltage (left one) and a low positive voltage (right one). *It should be noted* that the external switch 116 is coupled to the relative low power lines Vb1, Vb2, Vb3 in the pixel but not the driving power lines V1, V2, V3 ( see [0016]). The external switch 116 is used to change the electric potential difference ( see [0038]).

This external switch 116 does not teach the connection manner about the voltage terminal 510 being connected to a center of the power lines set to supply the driving high voltage, i.e.

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Vdd, as recited in the claimed invention.

Further, Yamazaki only discloses the circuit configuration but not discloses the actual connection structure. With respect to independent claim 7, Yamazaki failed to disclose that each voltage terminal is connected to the power supply through the same conductive material medium 704 in the present invention.

With respect to claim 13, Yamazaki further failed to disclose that the conductive material medium 704 is a conductive material interface in external connection.

With respect to claim 14, Yamazaki further failed to disclose the features as applied to independent claim 1.

For at least the foregoing reasons, Applicant respectfully submits that independent claims 1 and 7 patently define over the prior art, and should be allowed. For at least the same reasons, dependent claims 2-6 and 8-14 patently define over the prior art as well. Dependent claims 13-14 further define over the prior art.

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**CONCLUSION**

For at least the foregoing reasons, it is believed that all the pending claims 1-14 of the invention patentably define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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Respectfully submitted,

Belinda Lee

Belinda Lee

Registration No.: 46,863

Jianq Chyun Intellectual Property Office  
7<sup>th</sup> Floor-1, No. 100  
Roosevelt Road, Section 2  
Taipei, 100  
Taiwan  
Tel: 011-886-2-2369-2800  
Fax: 011-886-2-2369-7233  
Email: [belinda@jcipgroup.com.tw](mailto:belinda@jcipgroup.com.tw)  
[Usa@jcipgroup.com.tw](mailto:Usa@jcipgroup.com.tw)